

**CLAIMS:**

1. A method comprising:
  - 2 evaluating a load mismatch criterion relative to a wireless transmitter; and
  - 4 configuring a power amplifier associated with the wireless transmitter as a function of the load mismatch criterion.
2. The method of claim 1, further comprising:
  - 2 detecting a transmitted power signal and a reflected power signal; and
  - 4 calculating the load mismatch criterion as a function of the transmitted and reflected power signals.
3. The method of claim 2, further comprising separating a power signal into
  - 2 the transmitted power signal and the reflected power signal.
4. The method of claim 1, wherein configuring the power amplifier
  - 2 comprises configuring a gain of the power amplifier.
5. A method comprising:
  - 2 receiving at least one of a transmitted power signal level and a reflected power signal level from a power amplifier associated with a wireless transmitter; and
  - 4 configuring a gain of the power amplifier as a function of the transmitted and reflected power signal levels.
6. The method of claim 5, further comprising detecting at least one of a
  - 2 transmitted power signal and a reflected power signal.
7. The method of claim 6, further comprising separating a power signal into
  - 2 the transmitted power signal and the reflected power signal.

8. A processor readable medium containing processor executable instructions  
2 for:  
evaluating a load mismatch criterion relative to a wireless transmitter; and  
4 configuring a power amplifier associated with the wireless transmitter as a  
function of the load mismatch criterion.

9. The processor readable medium of claim 8, containing further processor  
2 executable instructions for:  
receiving a transmitted power signal level and a reflected power signal level;  
4 and  
calculating the load mismatch criterion as a function of the transmitted and  
6 reflected power signals.

10. The processor readable medium of claim 8, containing further processor  
2 executable instructions for configuring a gain of the power amplifier.

11. A processor readable medium containing processor executable instructions  
2 for:  
receiving at least one of a transmitted power signal level and a reflected power  
4 signal level from a power amplifier associated with a wireless transmitter; and  
configuring a gain of the power amplifier as a function of the transmitted and  
6 reflected power signal levels.

12. A wireless communication device comprising:  
2 a wireless transmitter;  
a power amplifier to output a signal from the wireless transmitter; and  
4 a controller to configure the power amplifier as a function of a load mismatch  
criterion determined from the signal.

13. The wireless communication device of claim 12, wherein the controller  
2 configures a gain of the power amplifier as a function of the load mismatch criterion.

2 14. The wireless communication device of claim 12, wherein the controller is  
configured to calculate the load mismatch criterion as a function of a transmitted power  
signal level and a reflected power signal level determined from the signal.

2 15. The wireless communication device of claim 12, further comprising a  
dual-directional coupler to separate the signal into a transmitted power signal component  
and a reflected power signal component.

2 16. The wireless communication device of claim 15, further comprising:  
a first power detector coupled to receive the transmitted power signal  
component and configured to generate a transmitted power signal level; and  
4 a second power detector coupled to receive the reflected power signal  
component and configured to generate a reflected power signal level.

2 17. The wireless communication device of claim 16, wherein at least one of  
the first and second power detectors comprises a broadband power detector.

2 18. The wireless communication device of claim 16, wherein the controller is  
configured to receive the transmitted and reflected power signal levels.

2 19. An integrated circuit comprising:  
a power amplifier to output a signal from a wireless transmitter; and  
a controller to configure the power amplifier as a function of a load mismatch  
4 criterion determined from the signal.

2 20. The integrated circuit of claim 19, wherein the controller configures a gain  
of the power amplifier as a function of the load mismatch criterion.

2 21. The integrated circuit of claim 19, wherein the controller is configured to  
calculate the load mismatch criterion as a function of a transmitted power signal level and  
a reflected power signal level determined from the signal.

22. The integrated circuit of claim 19, further comprising a dual-directional  
2 coupler to separate the signal into a transmitted power signal component and a reflected  
power signal component.

23. The integrated circuit of claim 22, further comprising:  
2 a first power detector coupled to receive the transmitted power signal  
component and configured to generate a transmitted power signal level; and  
4 a second power detector coupled to receive the reflected power signal  
component and configured to generate a reflected power signal level.

24. The integrated circuit of claim 23, wherein at least one of the first and  
2 second power detectors comprises a broadband power detector.

25. The integrated circuit of claim 23, wherein the controller is configured to  
2 receive the transmitted and reflected power signal levels.

26. An apparatus comprising:  
2 a power amplifier;  
a dual-directional coupler to separate a power signal into a transmitted power  
4 signal component and a reflected power signal component;  
a first power detector to generate a transmitted power signal level;  
6 a second power detector to generate a reflected power signal level; and  
a control arrangement to configure the power amplifier as a function of the  
8 transmitted and reflected power signal levels.

27. An apparatus comprising:  
2 a power amplifier;  
a directional coupler to extract a reflected power signal component from a  
4 power signal;  
a reverse power detector to generate a reflected power signal level; and

6           a control arrangement to configure the power amplifier as a function of the  
reflected power signal level.

28. An apparatus comprising:

2           a wireless transmitter;

          a power amplifier to output a signal from the wireless transmitter; and

4           a controller configured to

          evaluate a load mismatch criterion relative to the wireless transmitter, and

6           configure the power amplifier as a function of the load mismatch criterion.

29. The apparatus of claim 28, wherein the controller is further configured to:

2           detect a transmitted power signal and a reflected power signal; and

          calculate the load mismatch criterion as a function of the transmitted and

4           reflected power signals.

30. An apparatus comprising:

2           means for evaluating a load mismatch criterion relative to a wireless transmitter;

and

4           means for configuring a power amplifier associated with the wireless transmitter

as a function of the load mismatch criterion.

31. The apparatus of claim 30, further comprising:

2           means for detecting a transmitted power signal emitted by an antenna associated

with the wireless transmitter and a reflected power signal reflected by the antenna toward

4           the power amplifier; and

          means for calculating the load mismatch criterion as a function of the

6           transmitted and reflected power signals.

32. The apparatus of claim 31, further comprising means for separating a

2           power signal into the transmitted power signal and the reflected power signal.

33. The apparatus of claim 30, further comprising means for configuring a  
2 gain of the power amplifier.

34. An apparatus comprising:  
2 means for receiving at least one of a transmitted power signal level and a  
reflected power signal level from a power amplifier associated with a wireless  
4 transmitter; and  
means for configuring a gain of the power amplifier as a function of the  
6 transmitted and reflected power signal levels.

35. The apparatus of claim 34, further comprising means for detecting at least  
2 one of a transmitted power signal and a reflected power signal.

36. The apparatus of claim 35, further comprising means for separating a  
2 power signal into the transmitted power signal and the reflected power signal.